PCN Number:	20181105001.0	PCN Date:	November 05, 2018
Title:	Datasheet for ADS8691, ADS8695, ADS8699, ADS8671, ADS8675, ADS8661, ADS8665		
Customer Contact:	PCN Manager	**Dept:**	Quality Services

Change Type:

☐	Assembly Site	☐	Design	☐	Wafer Bump Site
☐	Assembly Process	☒	Data Sheet	☐	Wafer Bump Material
☐	Assembly Materials	☐	Part number change	☐	Wafer Bump Process
☐	Mechanical Specification	☐	Test Site	☐	Wafer Fab Site
☐	Packing/Shipping/Labeling	☐	Test Process	☐	Wafer Fab Materials
				☐	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.
The product datasheet(s) is being updated as summarized below.
The following change history provides further details.

TEXAS INSTRUMENTS

ADS8691, ADS8695, ADS8699
SBAS777A – DECEMBER 2016 – REVISED OCTOBER 2018

TEXAS INSTRUMENTS

ADS8671, ADS8675
SBAS779A – DECEMBER 2016 – REVISED OCTOBER 2018

Changes from Original (December 2016) to Revision A   **Page**

- Deleted *per channel* from *ALARM → High, Low Threshold* bullet in *Features* section 1
- Deleted WQFN package option from document 1
- Deleted RUM (WQFN) information from *Pin Configuration and Functions* section 3
- Deleted *offers a low impedance of 30 kΩ* from footnotes 2 and 3 in *Absolute Maximum Ratings* table 4
- Deleted RUM (WQFN) column from *Thermal Information* table 4
- Changed test conditions of Input Overvoltage Protection Circuit, V_{OVP} parameter 5
- Deleted WQFN row from V_{REFIO} and dV_{REFIO}/dT_A parameters 7
- Deleted multichannel reference from *Overview* section 21
- Changed *the input voltage range for each analog channel* to *the input voltage range* in *Analog Input Structure* section .. 22
- Changed *Input Overvoltage Protection Limits When AVDD = 5 V* table name from *Input Overvoltage Protection Limits When AVDD = 5 V or Offers a Low Impedance of 30 kΩ* 23
- Changed *AVDD is floating with an impedance 30 kΩ* to *AVDD is floating* in *Input Protection Circuit* section 24
- Changed *Input Overvoltage Protection Limits When AVDD = Floating* table title from *Input Overvoltage Protection Limits When AVDD = Floating with Impedance 30 kΩ* 24
- Deleted RUM (WQFN) package information from *External Reference* section 30
- Added footnotes to *List of Input Commands* table 41

TEXAS INSTRUMENTS

ADS8661, ADS8665
SBAS780A – DECEMBER 2016 – REVISED OCTOBER 2018

The datasheet number will be changing.

Device Family	Change From:	Change To:
ADS8691, ADS8695, ADS8699	SBAS777	SBAS777A
ADS8671, ADS8675	SBAS779	SBAS779A
ADS8661, ADS8665	SBAS780	SBAS780A

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/ADS8691

http://www.ti.com/product/ADS8671

http://www.ti.com/product/ADS8661

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

ADS8691IPW	ADS8691IPWR	ADS8695IPW	ADS8695IPWR
ADS8699IPW	ADS8699IPWR	ADS8671IPW	ADS8671IPWR
ADS8675IPW	ADS8675IPWR	ADS8661IPW	ADS8661IPWR
ADS8665IPW	ADS8665IPWR	ADS8661IPW	ADS8661IPWR
ADS8665IPW	ADS8665IPWR		

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
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